

[illegible]

1

Page43 of 52

Figures

Figure 1: A schematic diagram illustrating the experimental setup for measuring the time delay of a signal. The diagram shows a signal source (S) connected to a delay line (DL) and a detector (D). The signal source is connected to the delay line, which is connected to the detector. The delay line is labeled with a time delay τ . The signal source is labeled with a frequency f . The detector is labeled with a time delay τ . The diagram shows the signal path from the source to the detector, with the delay line in between. The signal source is connected to the delay line, which is connected to the detector. The delay line is labeled with a time delay τ . The signal source is labeled with a frequency f . The detector is labeled with a time delay τ .